

MADDOG LVAR	LUA FILE	FS DATA TYPE	FSUIPC SIZE	FSUIPC OFFSET	MF TYPE	MF SIZE	MF MASK	MF COMP?	NOTES
md_ipc_ap_alt	MD_FGCP_PWR	number	str5	0x6A0C	str	n/a	n/a	substr	substring[1 thru 5] (or 0 thru 4) ?? needs testing Also 7 segment mode dependant, see settings in lua
md_ipc_ap_hdg	MD_FGCP_PWR	number	str3	0x6A03	str	n/a	n/a	substr	substring[1 thru 3] (or 0 thru 2) ?? needs testing
md_ipc_ap_spd	MD_FGCP_PWR	number	str4	0x69FC	str	n/a	n/a	substr	substring[1 thru 4] (or 0 thru 3) ?? needs testing
md_ipc_ap_vs (7 seg mode off)	MD_FGCP_PWR	number	str4	0x6A08	str	n/a	n/a	substr	substring[1 thru 4] (or 0 thru 3) ?? needs testing Also 7 segment mode dependant, see settings in lua
md_ipc_ap_vs (7 seg mode on)	MD_FGCP_PWR	number	str6	0x6A06	str	n/a	n/a	substr	substring[1 thru 6] (or 0 thru 5) ?? needs testing Also 7 segment mode dependant, see settings in lua
md_ipc_FGCP spcl Alt	MD_FGCP_PWR	n/a	ub	0x6A13	int	1	0x01	no	when < 10,000 feet and >= 1000 feet (could be used to drive an alpha-numeric "=")
md_ipc_FGCP spcl Alt	MD_FGCP_PWR	n/a	ub	0x6A13	int	1	0x02	no	< 1000 feet (could be used to drive an alpha-numeric "==")
md_ipc_FGCP spcl Speed	MD_FGCP_PWR	n/a	ub	0x6A11	int	1	0x01	no	1 when in mach mode (could be used to drive a decimal point led)
md_ipc_FGCP spcl VS	MD_FGCP_PWR	n/a	ub	0x6A12	int	1	0xFF	yes	compare[if = 0 then 1 else 0] in VS mode and VS is zero (could be used to drive an alpha-numeric "V ")
md_ipc_FGCP spcl VS	MD_FGCP_PWR	n/a	ub	0x6A12	int	1	0x01	no	in VS mode and VS is positive (could be used to drive an alpha-numeric "V+")
md_ipc_FGCP spcl VS	MD_FGCP_PWR	n/a	ub	0x6A12	int	1	0x02	no	in VS mode and VS is negative (could be used to drive an alpha-numeric "V-")
md_ipc_FGCP spcl VS	MD_FGCP_PWR	n/a	ub	0x6A12	int	1	0x03	no	in IAS mode (could be used to drive an alpha-numeric "S ")
md_ipc_FGCP spcl VS	MD_FGCP_PWR	n/a	ub	0x6A12	int	1	0x04	no	in mach mode (could be used to drive a decimal point led, an alpha-numeric "M " or an alpha-numeric "M .")
md_ipc_FGCP spcl VS	MD_FGCP_PWR	n/a	ub	0x6A12	int	1	0x05	no	in pitch mode and VS is zero (could be used to drive an alpha-numeric "P ")
md_ipc_FGCP spcl VS	MD_FGCP_PWR	n/a	ub	0x6A12	int	1	0x06	no	in pitch mode and VS is positive (could be used to drive an alpha-numeric "P+")
md_ipc_FGCP spcl VS	MD_FGCP_PWR	n/a	ub	0x6A12	int	1	0x07	no	in pitch mode and VS is negative (could be used to drive an alpha-numeric "P-")
ovhd_ac_busxtie_switch1	MD_FGCP_PWR	bool	ub	0x6914	int	1	0x01	no	also used for FGCP power monitoring logic
ovhd_acload_needle1	MD_FGCP_PWR	number	ud	0x690C	flt	4	n/a	no	also used for FGCP power monitoring logic
ovhd_acload_needle3	MD_FGCP_PWR	number	ud	0x6910	flt	4	n/a	no	also used for FGCP power monitoring logic
ovhd_L_apu_bus_adv1	MD_FGCP_PWR	bool	ud*	0x6988	int	4	0x01	no	also used for FGCP power monitoring logic
ovhd_L_ext_bus_adv1	MD_FGCP_PWR	bool	ud*	0x6890	int	4	0x01	no	also used for FGCP power monitoring logic